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**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
T2147-907715

In Re Application Of: Sylvie DESMANNE ET AL.

Serial No.  
10/075,289

Filing Date  
February 15, 2002

Examiner

Group-Art Unit  
2183

Title: Coherence Controller for a Multiprocessor System, Module, and Multiprocessor System with a Multimodule Architecture Incorporating Such a Controller

Address to:  
Assistant Commissioner for Patents  
Washington, D.C. 20231

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**37 CFR 1.97(b)**

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

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Coherence Controller for a Multiprocessor System, Module, and Multiprocessor System with a  
Architecture Incorporating Such a Controller

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**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of \_\_\_\_\_ is attached.
- ☒ The Assistant Commissioner is hereby authorized to charge and credit Deposit Account No. 501165 as described below. A duplicate copy of this sheet is enclosed.
- ☐ Charge the amount of \_\_\_\_\_
- ☐ Credit any overpayment.
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Signature

Dated: July 11, 2002



Edward J. Kondracki  
MILES & STOCKBRIDGE P.C.  
Suite 500  
1751 Pinnacle Drive  
McLean, VA 22102-3833  
Tel. 703/903-9000  
Fax 703/610-8686

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CC:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Sylvie LESMANNE ET AL.

Serial No.: 10/075,289

Filed: February 15, 2002

For: **COHERENCE CONTROLLER FOR A  
MULTIPROCESSOR SYSTEM,  
MODULE, AND MULTIPROCESSOR  
SYSTEM WITH A MULTIMODULE  
ARCHITECTURE INCORPORATING  
SUCH A CONTROLLER**

Examiner:

Group Art Unit: 2183

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McLean, Virginia

**SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure set forth in 37 CFR 1.56, it is requested that the documents listed on the attached Information Disclosure Statement By Applicant be made of record in this application.

Enclosed are:

- 1) the "Notification du rapport de recherché europeen" ("Communication of the European Search Report) issued by the EPO on May 31, 2002;
- 2) the EP Search Report, in which the document EP 0 881 579 A of BULL S.A. has been cited in addition to the documents cited in the French Search Report and classified in Category A assigned to technical background documents; and
- 3) the EP Patent Application No. 0 881 579 A1, which corresponds to U.S. Patent No. 6,148,378 (Bordaz et al.) (Our Ref. T2147-905888).

In compliance with the requirements of 37 CFR 1.97 and 1.98, the possible relevance of the documents is noted on the attached Information Disclosure Statement by Applicant. Copies of the documents are enclosed herewith.

Without any assertion of prior art effect, the documents on the Information Disclosure Statement by Applicant are called to the Examiner's attention. Although the relevance of each document has been briefly indicated, the Examiner is urged to make his own determination of relevance and no representation is made nor intended that "better" art is not available.

Respectfully submitted,

Miles & Stockbridge P.C.

Date July 11, 2002

*JK*

By:



Edward J. Kondracki  
Registration No. 20,604

Miles & Stockbridge, P.C.  
1751 Pinnacle Drive, Suite 500  
McLean, Virginia 22102-3833  
Tel.: (703) 903-9000